

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yasutaka NAKASHIBA

Conf. 2273

Application No. 10/812,282

Group 2815

Filed March 30, 2004

Examiner J. Jackson, Jr.

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

PETITION FOR EXTENSION OF TIME

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 4, 2010

Sir:

The undersigned hereby petitions for an extension of time to respond to the Official Action of March 8, 2010 for two months to August 8, 2010.

Please charge the extension fee of \$490.00 to our credit card.

A responsive paper is filed herewith.

Respectfully submitted,

YOUNG & THOMPSON

/Thomas W. Perkins/

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TWP/lrs